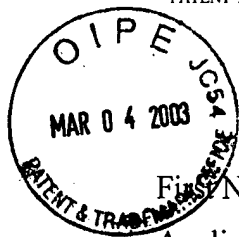




32692

PATENT TRADEMARK OFFICE



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: GEISSINGER, JOHN D.

Application No.: 09/491302

Filed: January 25, 2000

Title: ELECTRONIC PACKAGE WITH INTEGRATED CAPACITOR

Group Art Unit: 2815

Examiner: Paul E. Brock, II

Case No.: 55271US002

Patent

#20/C

3/11/03
AdmithAMENDMENT AND RESPONSECommissioner for Patents
Washington, DC 20231

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on:

2-20-03

Date

Sandra Barnett
Signed by: SONDR A BARNETT

Dear Sir:

This response is to the Official Action with the mailing date of November 20, 2003.

Please amend this application as follows:

In the Specification:

Please amend the paragraph starting at p. 3, line 17 as follows:

The capacitors are preferably thin-film parallel-plate type capacitors. Preferred dielectric materials for the capacitors include barium titanate, strontium titanate, and a polymer, e.g., a non-conductive polymer, blended with high dielectric constant particles such as barium titanate, barium strontium titanate, titanium oxide, lead zirconium titanate and tantalum oxide. The capacitance of a typical thin film, parallel-plate capacitor is from about 1 nF/sq. cm. to about 100 nF/sq. cm.

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